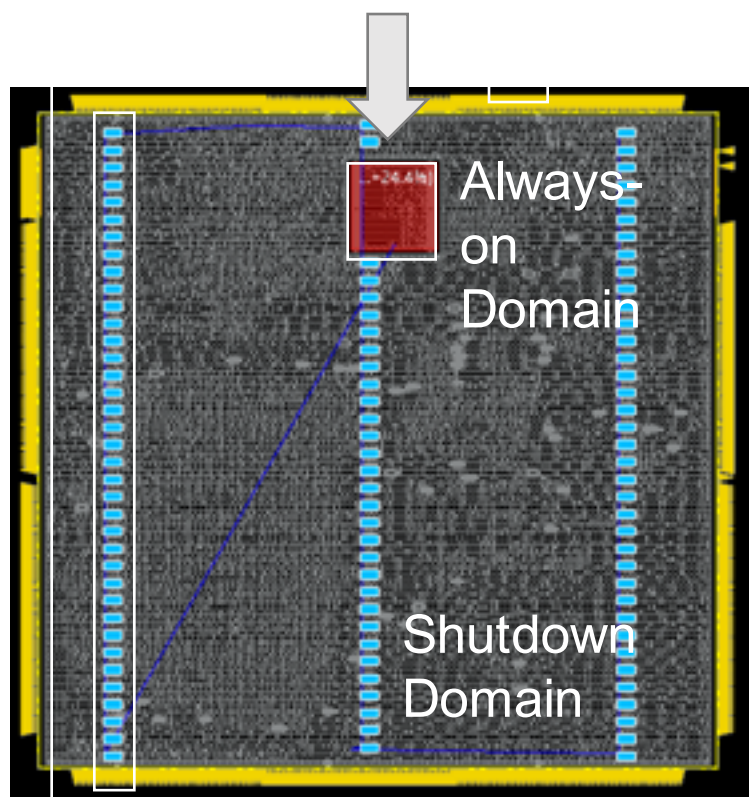


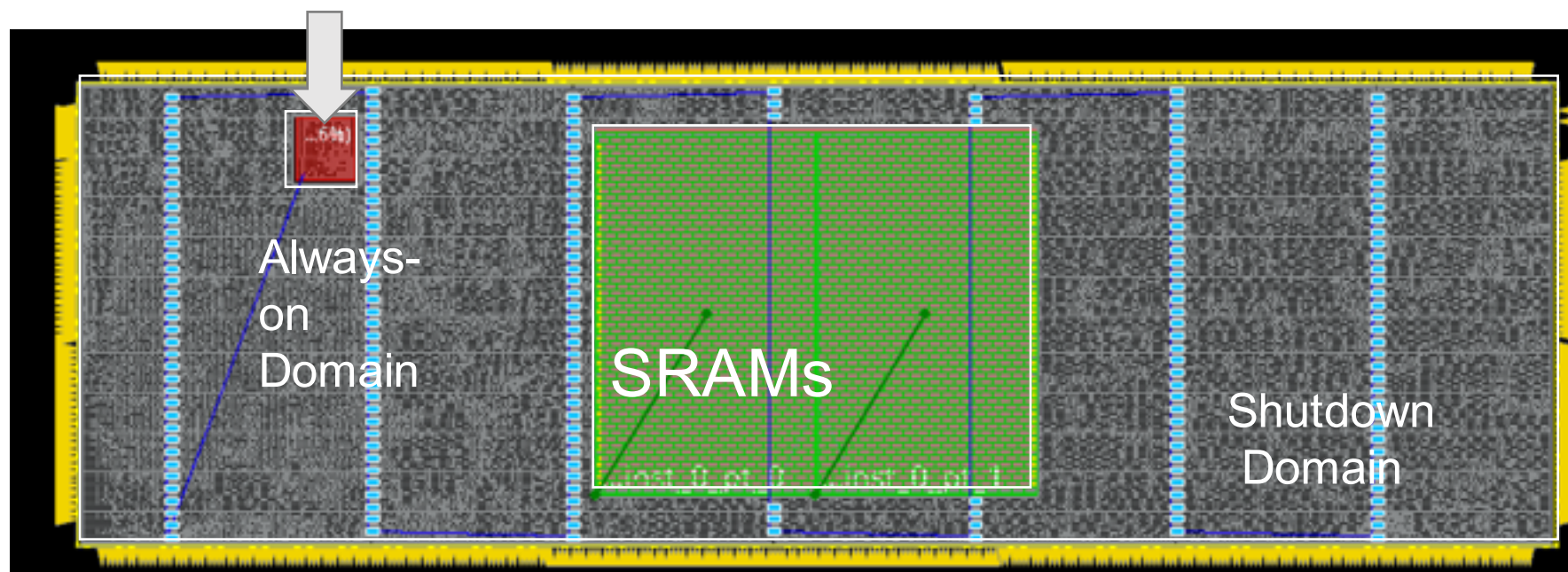
1

AON region must be designed and placed carefully to avoid DRC



3

Properties and static checks are re-used for AON region in a different design



2

Code must be written with intent-implementation split

```
array set mflowgen.property.even_y {
  property "((aon_llx//y_pitch) % 2 == 0) & ((aon_ury//y_pitch) % 2 == 0)"
  describe "The AON region must begin and end on an even numbered
    standard cell row to prevent obstructed power switches and
    LUP DRCs"
}
```

```
array set mflowgen.property.max_width {
  property "(aon_urx - aon_llx) < horiz_switch_pitch)"
  describe "The always on region must be narrower than the pitch
    between power switch pitches so that no more than 1 column
    of switches is obstructed"
}
```